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पेटेंट कार्यालय, भारत सरकार

The Patent Office, Government Of India

डिजाइन के पंजीकरण का प्रमाण पत्र | Certificate of Registration of Design

डिजाइन सं. / Design No. : 440430-001
 तारीख / Date : 12/12/2024
 पारस्परिकता तारीख / Reciprocity Date* :
 देश / Country :

प्रमाणित किया जाता है कि संलग्न प्रति में वर्णित डिजाइन जो **ENGLISH WRITING ERROR DETECTING DEVICE** से संबंधित है, का पंजीकरण, श्रेणी 14-02 में 1.Dr.M.Sudheer Kumar 2. Juthuka Kalavathi 3.Dr. Sampath Kumar Chavvakula 4.Dr.Kadiyam Sajaya Kumari 5.Kuchipudi Vijana 6.Dadala Nagababu 7.Dr.P.Lakshmi Narasa Dasu 8.K.Krishna Rao 9.Dr.D.Nagaraja Kumari 10.K. Aruna के नाम में उपर्युक्त संख्या और तारीख में कर लिया गया है।

Certified that the design of which a copy is annexed hereto has been registered as of the number and date given above in class 14-02 in respect of the application of such design to **ENGLISH WRITING ERROR DETECTING DEVICE** in the name of 1.Dr.M.Sudheer Kumar 2. Juthuka Kalavathi 3.Dr. Sampath Kumar Chavvakula 4.Dr.Kadiyam Sajaya Kumari 5.Kuchipudi Vijana 6.Dadala Nagababu 7.Dr.P.Lakshmi Narasa Dasu 8.K.Krishna Rao 9.Dr.D.Nagaraja Kumari 10.K. Aruna.

डिजाइन अधिनियम, 2000 तथा डिजाइन नियम, 2001 के अध्याधीन प्रावधानों के अनुसरण में।

In pursuance of and subject to the provisions of the Designs Act, 2000 and the Designs Rules, 2001.

जारी करने की तिथि : 20/03/2025
 Date of Issue :



अमिताभ सिंह
 उच्चाधिकारी

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(54) Title of the invention : Computer-Implemented System for Temporal and Spatial Analysis of Interstate Economic Disparities

(51) International classification	:G06Q 10/06, G06Q 30/02, G06Q 40/06, G06Q 40/00, G06Q 50/06	(71)Name of Applicant : 1)SRR & CVR Government Degree College (Autonomous) Address of Applicant :Machavaram, Vijayawada-520004, Andhra Pradesh. India, Vijayawada Andhra Pradesh India (72)Name of Inventor : 1)Dr. RAMESH KOLLETTI
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(57) Abstract :

The present invention relates to a computer-implemented system and method for empirical analysis of interstate economic disparities using Per Capita Gross State Domestic Product (PCGSDP) and associated socio-economic indicators. The system is configured to acquire historical state-wise economic datasets, preprocess and normalize the data, and temporally classify the data into pre-reform and post-reform periods based on a defined economic reform threshold. An integrated analytics engine computes growth rates, dispersion measures, and inequality indices to evaluate relative state performance. The invention further provides automated visualization and reporting modules to generate comparative charts, maps, and structured analytical outputs. By combining temporal comparison, spatial comparison, and standardized inequality measurement within a unified computational framework, the system enables objective, repeatable, and scalable assessment of regional economic divergence and convergence. The invention supports policymakers, researchers, and planners in evidence-based decision-making aimed at balanced and inclusive economic development.

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